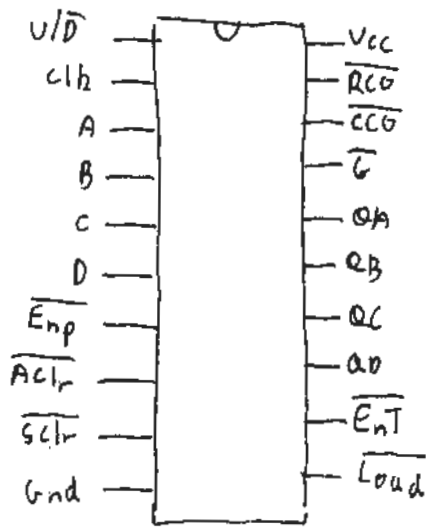
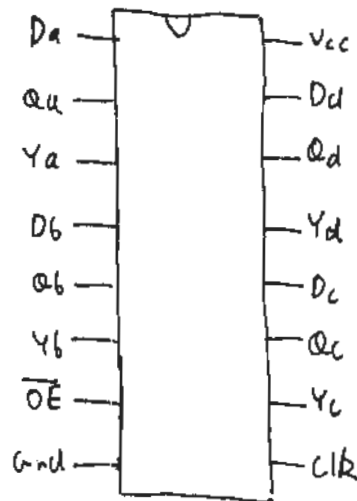


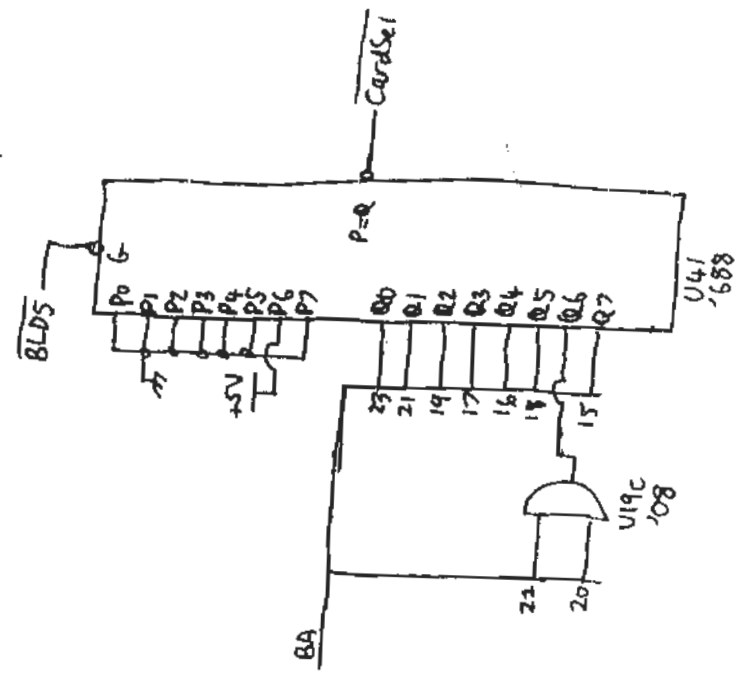
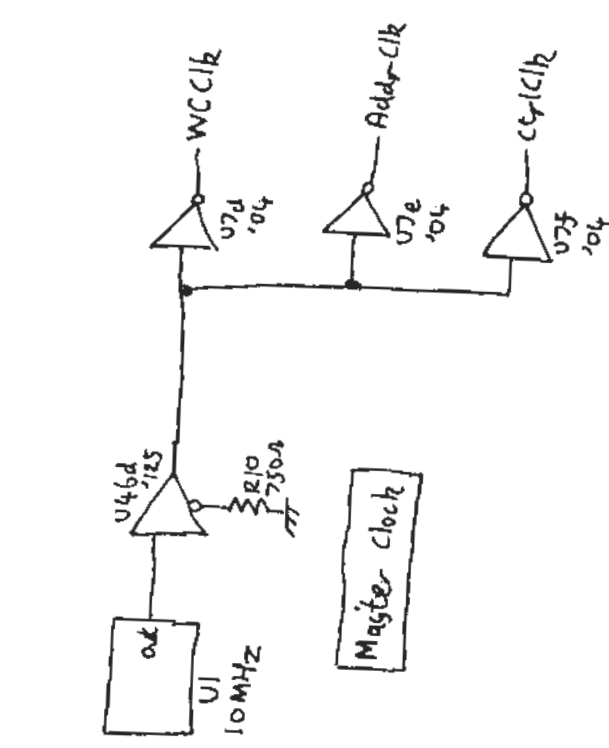
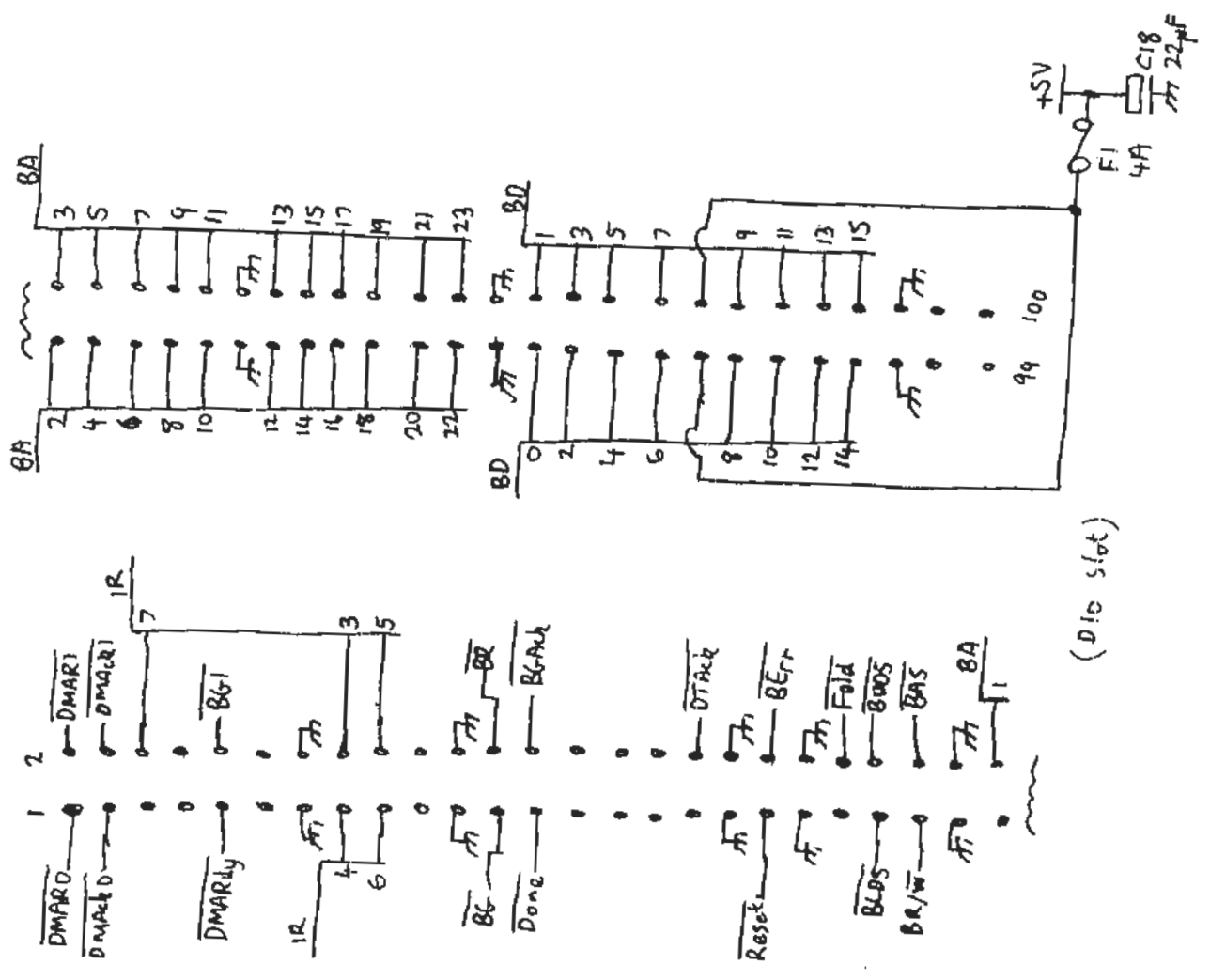
98620

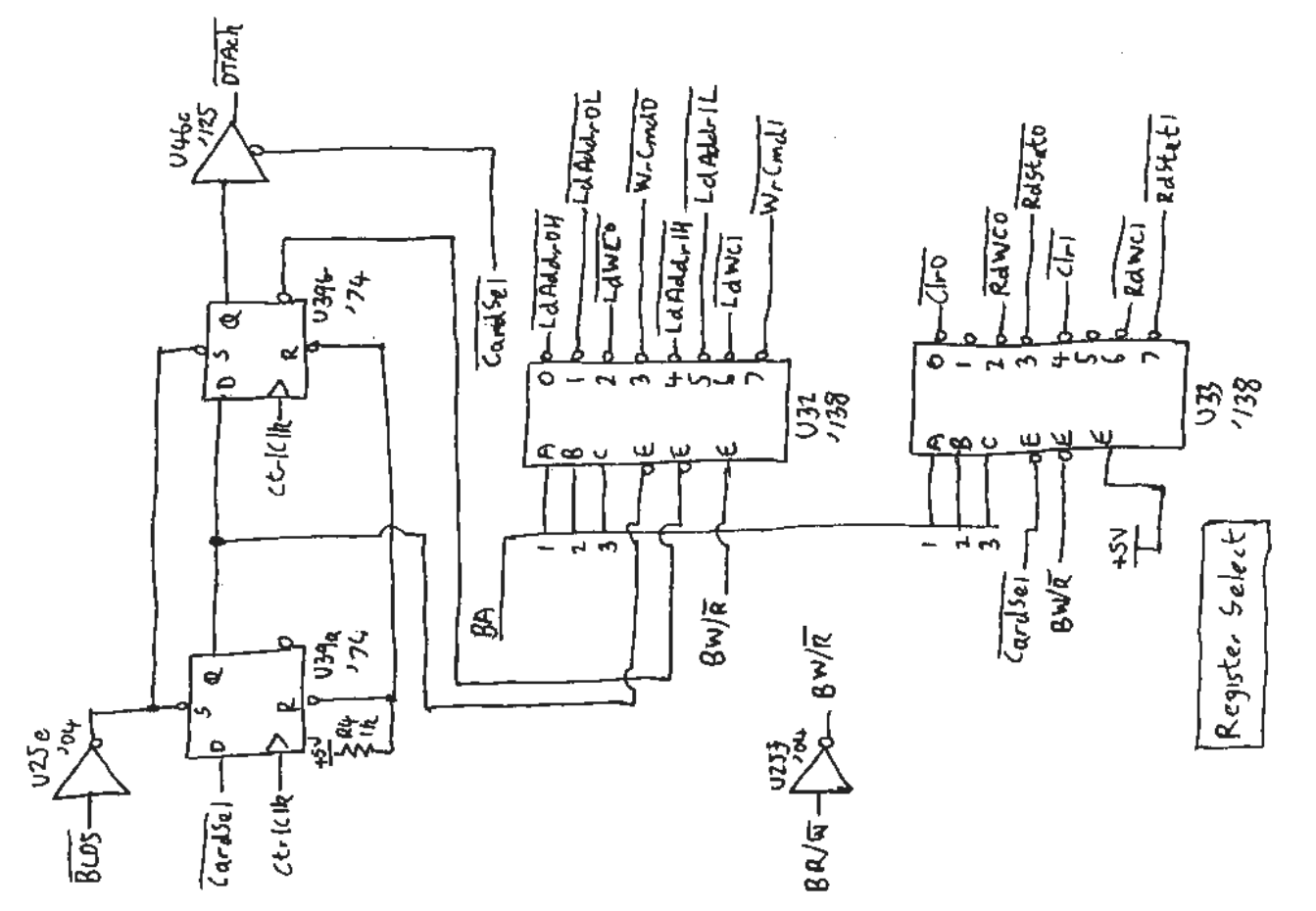
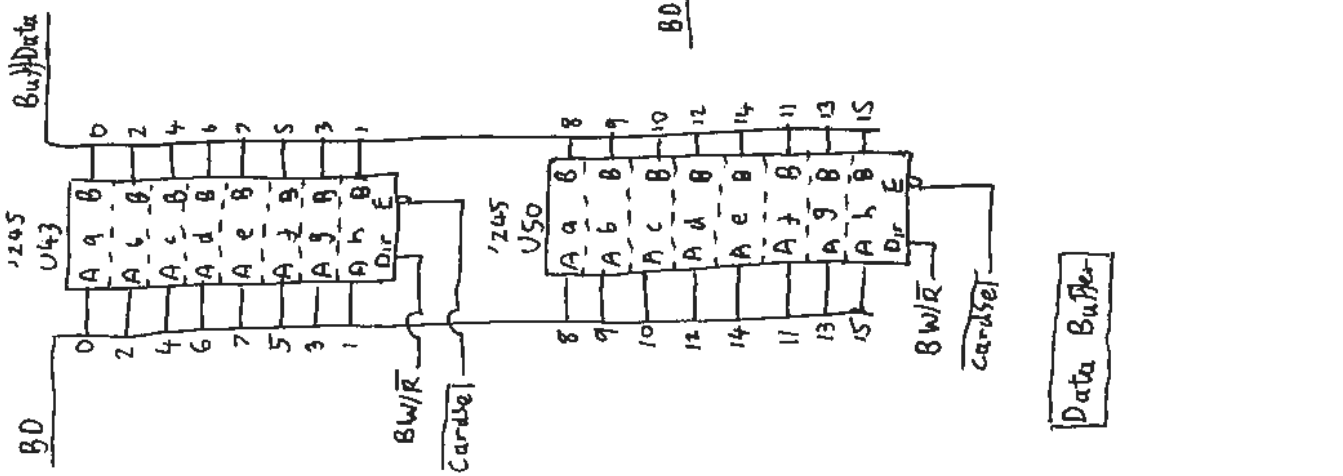


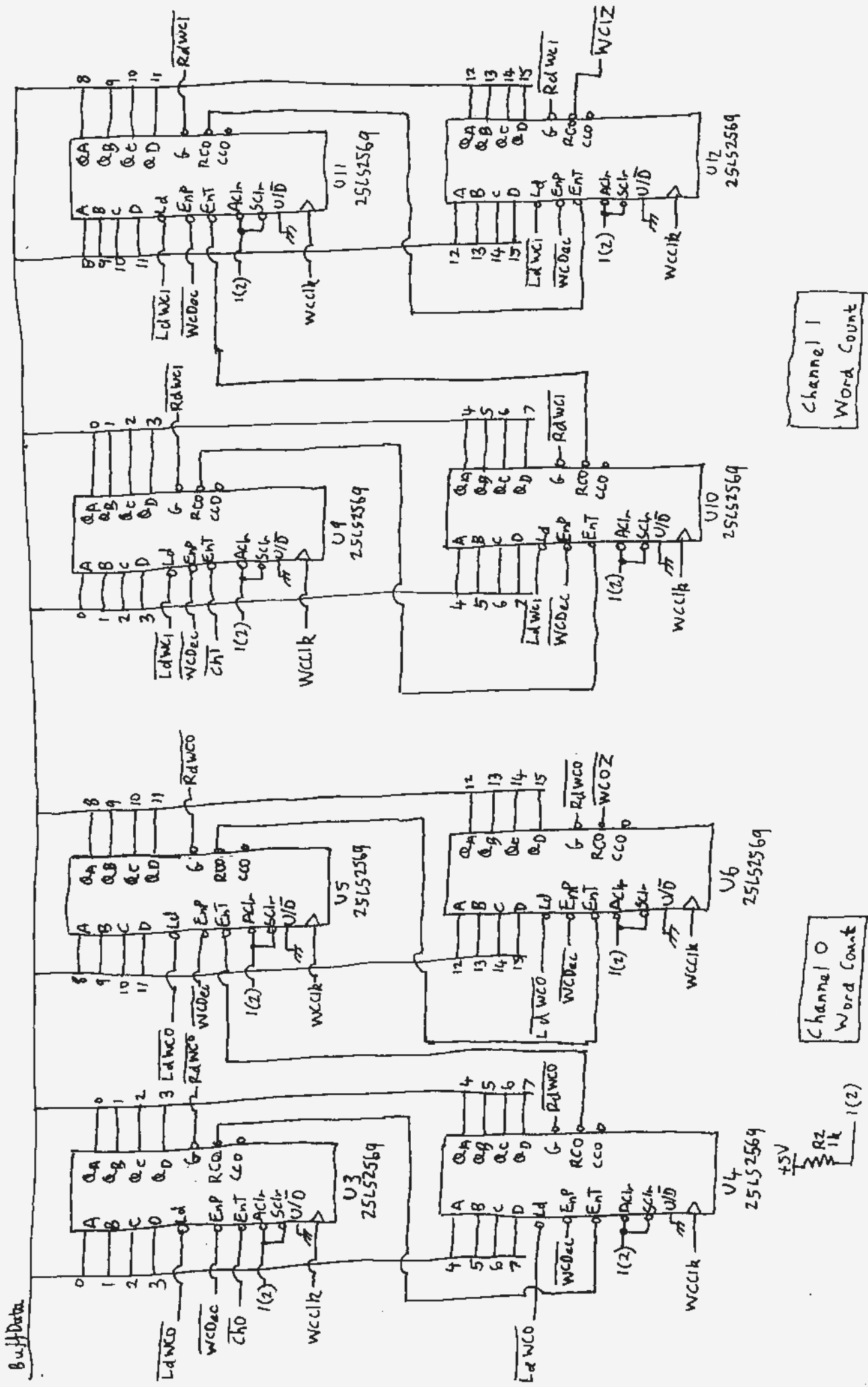
25LS2569  
Synchronous 4-bit Up/Down Counter  
with 3-state outputs



25LS2518  
Quad D-Registers with standard  
and 3-state outputs  
Q = Totem Pole  
Y = 3-State







(R/Bk)

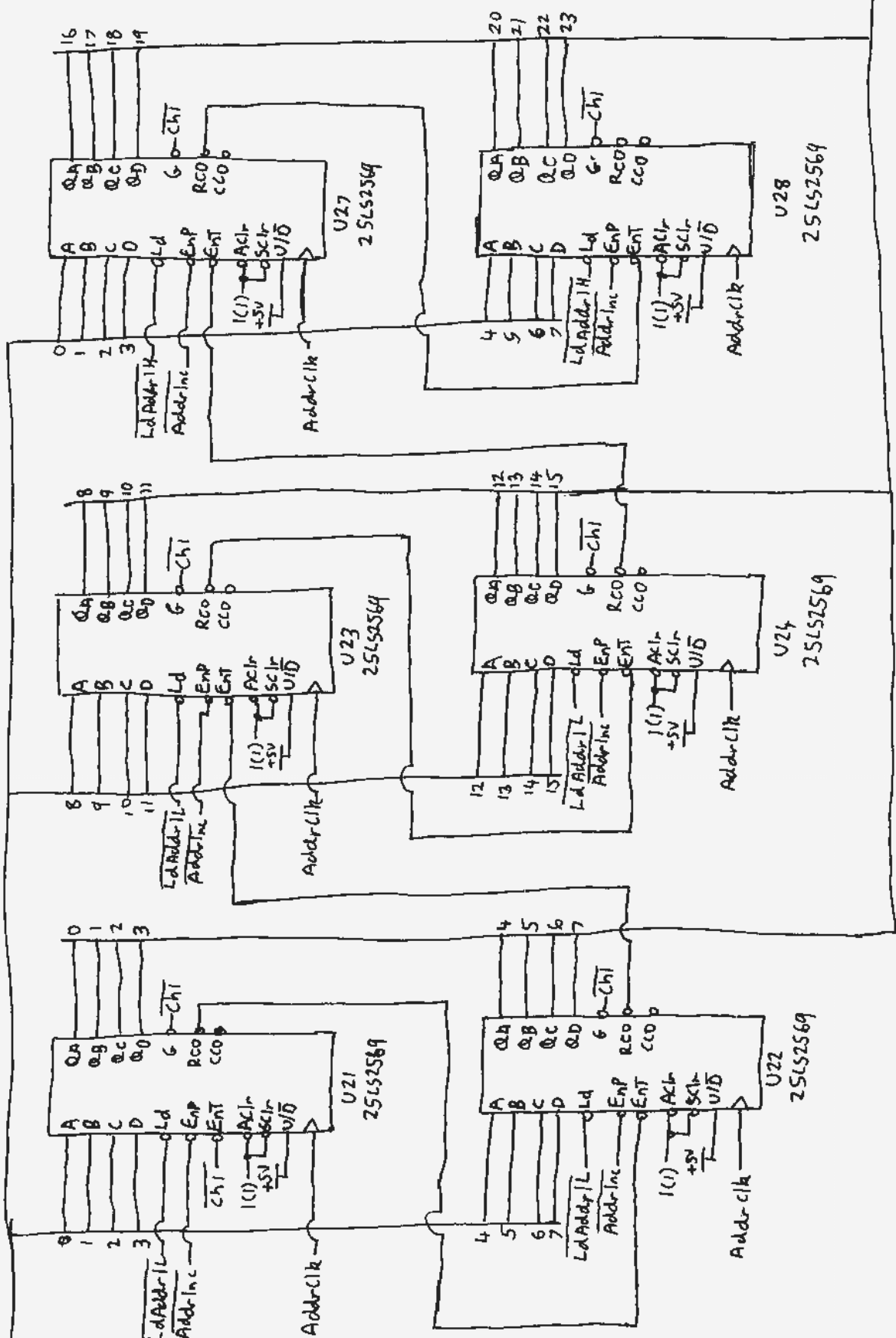
Sheet 3

DMA Controller

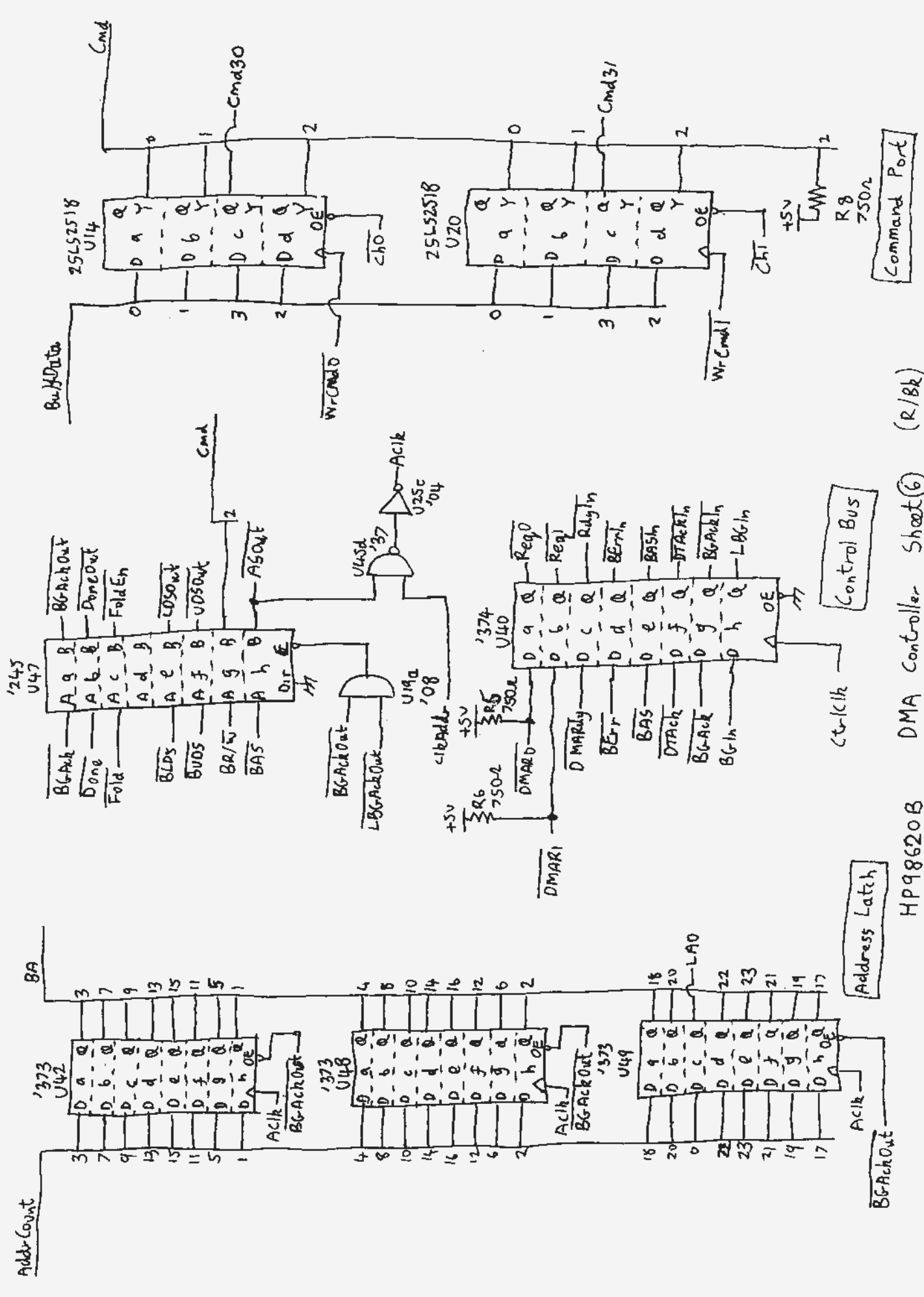
HP 986208



BufData



Channel 1 Address Counter



Command Port

(R/Bk)

DMA Controller Sheet 6

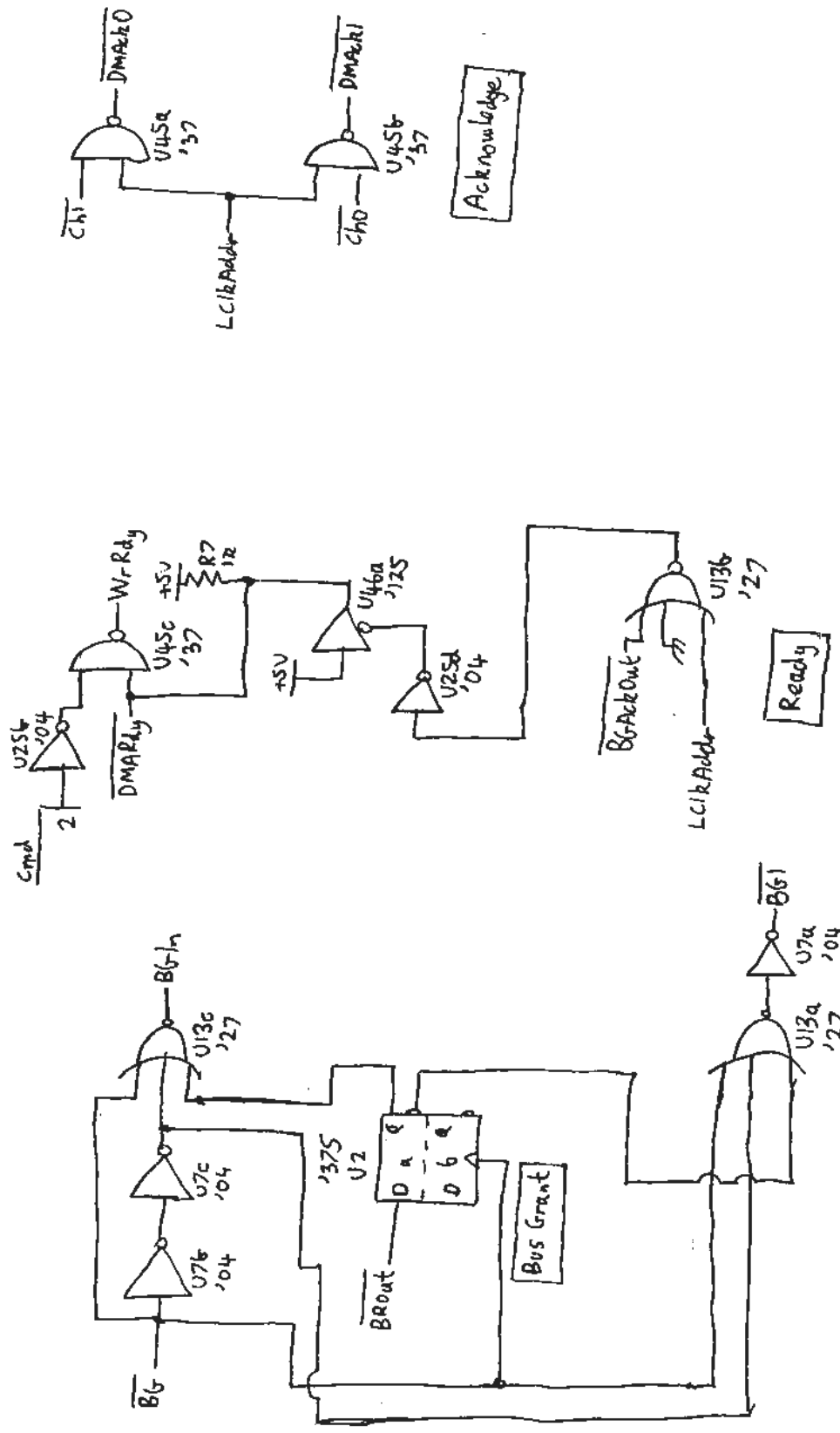
HP98620B

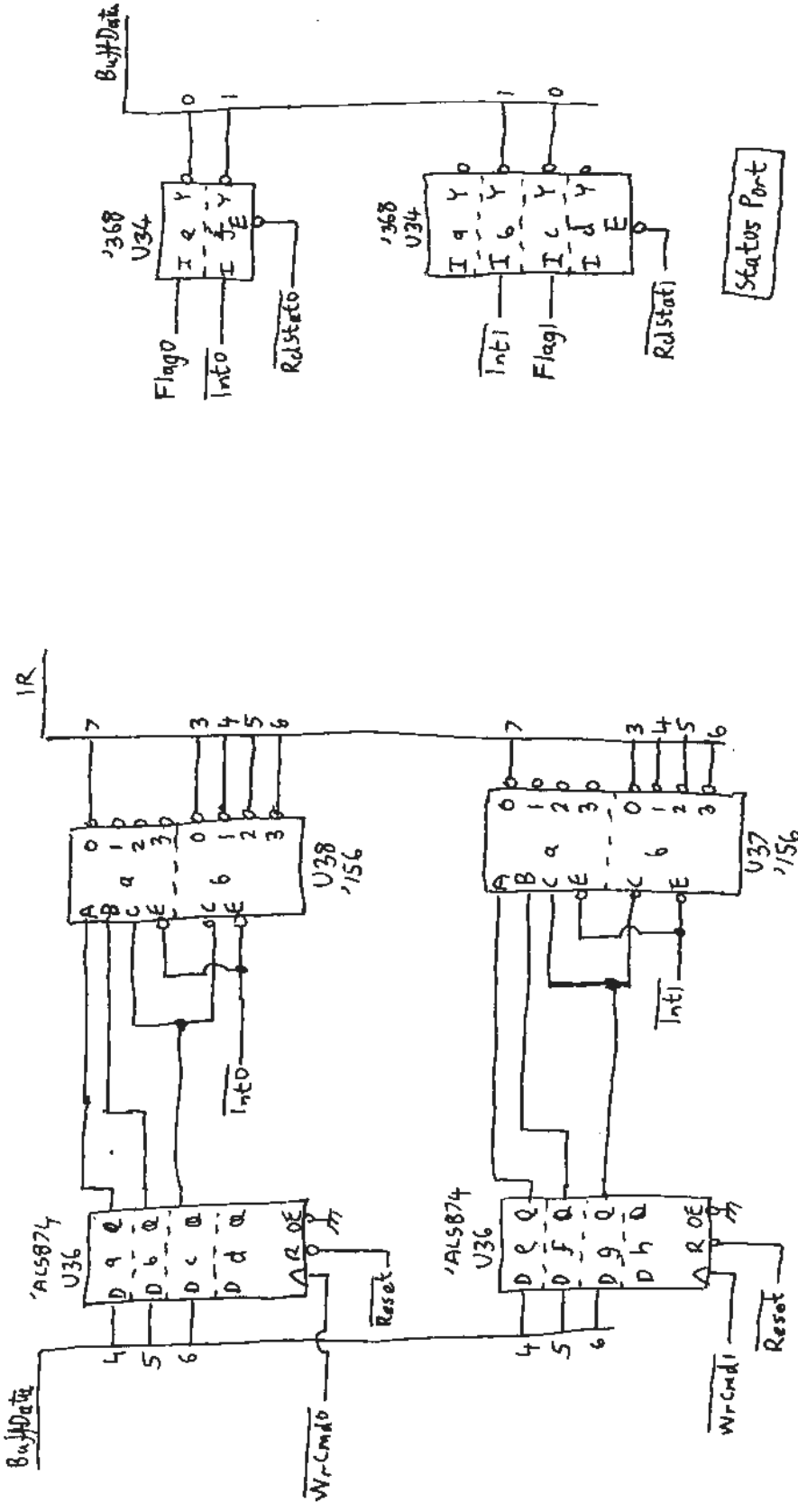
Address Latch

Control Bus









Interrupts